

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Albert Cosand, et al.

Serial No.: 10/707,076

Filed: November 19, 2003

For: PPM Receiving System and Method Using Time-Interleaved Integrators

Attorney Docket No.: BOE 0445 PUS

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**DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.56**

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Sir:

In compliance with 37 C.F.R. § 1.56, the art listed and identified on the attached PTO form is being submitted herewith for consideration by the Examiner. A copy of each of the listed references is included herewith.

It is Applicants' opinion that the claims presently on file patentably distinguish the present invention from each of these references. The above references are being cited only in the interests of candor and without any admission that they constitute statutory prior art or contain matter which anticipates the invention or which would render the same obvious, either singly or in combination, to a person of ordinary skill in the art.

No fee is believed to be due because this statement and enclosures are being filed before the first Office Action on the merits mailed by the PTO. The Commissioner is authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 50-0476.

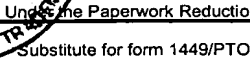
Respectfully submitted,



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Attorney for Applicant(s)

Date: February 18, 2004



Approved for use through 07/31/2006. OMB 0651-0031
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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number 10/707076

Filing Date 11-19-2003

First Named Inventor Albert Cosand

Art Unit Unknown

Examiner Name Unknown

Attorney Docket Number 03-0030

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	CHAN AND LAWRENCE, "An ASIC Chipset for Optical Communications", GaAs IC Symposium, 1993, pp. 303-306, Galaxy Microsystems, Inc., San Jose, California	
	AB	KARVONEN, RILEY, AND KOSTAMOVARA, "A Low Noise Quadrature Subsampling Mixer", IEEE International Symposium on Circuits & Systems, 2001, pp. IV 790 - IV 793, Department of Electrical Engineering and InfoTech Oulu, University of Oulu, 90571 Finland	
	AC	GHASSEMLOOY AND WICKRAMASINGHE, "A Low Cost Sub-Carrier Multiplexed PPM for Optical Fibre Transmission Systems", IEEE, 1998, pp.99-107, Contributed Paper, United Kingdom	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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